

# 4V Drive Nch+Pch MOSFET

AEC-Q101 Qualified

## SP8M21FRA

**●Structure**

Silicon N-channel MOSFET /  
Silicon P-channel MOSFET

**●Features**

- 1) Low on-resistance.
- 2) Built-in G-S protection diode.
- 3) Small and surface mount package (SOP8).

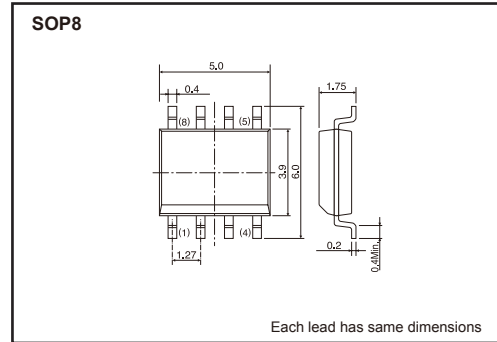
**●Applications**

Switching

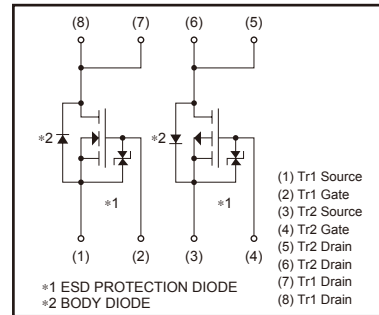
**●Package specifications**

Type	Package	Taping
	Code	TB
	Basic ordering unit (pieces)	2500
SP8M21FRA		○

**●Dimensions (Unit : mm)**



**●Inner circuit**



**●Absolute maximum ratings (Ta=25°C)**

Parameter	Symbol	Limits		Unit	
		Tr1 : N-ch	Tr2 : P-ch		
Drain-source voltage	$V_{DSS}$	45	-45	V	
Gate-source voltage	$V_{GSS}$	$\pm 20$	$\pm 20$	V	
Drain current	Continuous	$I_D$	$\pm 6.0$	$\pm 4.0$	A
	Pulsed	$I_{DP}^{*1}$	$\pm 24$	$\pm 16$	A
Source current (Body diode)	Continuous	$I_S$	1.0	-1.0	A
	Pulsed	$I_{SP}^{*1}$	24	-16	A
Total power dissipation	$P_D^{*2}$	2.0		W / TOTAL	
		1.4		W / ELEMENT	
Channel temperature	$T_{ch}$	150		°C	
Storage temperature	$T_{stg}$	-55 to +150		°C	

\*1  $P_w \leq 10\mu s$ , Duty cycle  $\leq 1\%$   
\*2 Mounted on a ceramic board.

## Transistors

## N-ch

## ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Gate-source leakage	I <sub>GSS</sub>	–	–	±10	μA	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V
Drain-source breakdown voltage	V <sub>(BR) DSS</sub>	45	–	–	V	I <sub>D</sub> = 1mA, V <sub>GS</sub> =0V
Zero gate voltage drain current	I <sub>DSS</sub>	–	–	1	μA	V <sub>DS</sub> = 45V, V <sub>GS</sub> =0V
Gate threshold voltage	V <sub>GS(th)</sub>	1.0	–	2.5	V	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1mA
Static drain-source on-state resistance	R <sub>DS(on)*</sub>	–	18	25	mΩ	I <sub>D</sub> = 6.0A, V <sub>GS</sub> = 10V
		–	24	34	mΩ	I <sub>D</sub> = 6.0A, V <sub>GS</sub> = 4.5V
		–	26	37	mΩ	I <sub>D</sub> = 6.0A, V <sub>GS</sub> = 4.0V
Forward transfer admittance	Y <sub>fs</sub>   *	6.0	–	–	S	V <sub>DS</sub> = 10V, I <sub>D</sub> = 6.0A
Input capacitance	C <sub>iss</sub>	–	1400	–	pF	V <sub>DS</sub> = 10V
Output capacitance	C <sub>oss</sub>	–	310	–	pF	V <sub>GS</sub> =0V
Reverse transfer capacitance	C <sub>rss</sub>	–	175	–	pF	f=1MHz
Turn-on delay time	t <sub>d(on)</sub> *	–	19	–	ns	V <sub>DD</sub> ≐ 25V I <sub>D</sub> = 3.0A
Rise time	t <sub>r</sub> *	–	30	–	ns	V <sub>GS</sub> = 10V
Turn-off delay time	t <sub>d(off)</sub> *	–	72	–	ns	R <sub>L</sub> = 8Ω
Fall time	t <sub>f</sub> *	–	27	–	ns	R <sub>G</sub> =10Ω
Total gate charge	Q <sub>g</sub> *	–	15.4	21.6	nC	V <sub>DD</sub> ≐ 25V, V <sub>GS</sub> = 5V
Gate-source charge	Q <sub>gs</sub> *	–	3.7	–	nC	I <sub>D</sub> = 6.0A
Gate-drain charge	Q <sub>gd</sub> *	–	6.5	–	nC	R <sub>L</sub> = 4Ω, R <sub>G</sub> = 10Ω

\*Pulsed

## ●Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward voltage	V <sub>SD</sub> *	–	–	1.2	V	I <sub>S</sub> = 6.0A, V <sub>GS</sub> =0V

\*Pulsed

## Transistors

## P-ch

## ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Gate-source leakage	I <sub>GSS</sub>	–	–	±10	μA	V <sub>GS</sub> = ±20V, V <sub>DS</sub> =0V
Drain-source breakdown voltage	V <sub>(BR) DSS</sub>	–45	–	–	V	I <sub>D</sub> = –1mA, V <sub>GS</sub> =0V
Zero gate voltage drain current	I <sub>DSS</sub>	–	–	–1	μA	V <sub>DS</sub> = –45V, V <sub>GS</sub> =0V
Gate threshold voltage	V <sub>GS(th)</sub>	–1.0	–	–2.5	V	V <sub>DS</sub> = –10V, I <sub>D</sub> = –1mA
Static drain-source on-state resistance	R <sub>DS(on)*</sub>	–	33	46	mΩ	I <sub>D</sub> = –4.0A, V <sub>GS</sub> = –10V
		–	43	60	mΩ	I <sub>D</sub> = –4.0A, V <sub>GS</sub> = –4.5V
		–	47	65	mΩ	I <sub>D</sub> = –4.0A, V <sub>GS</sub> = –4.0V
Forward transfer admittance	Y <sub>fs</sub>   *	6.0	–	–	S	V <sub>DS</sub> = –10V, I <sub>D</sub> = –4.0A
Input capacitance	C <sub>iss</sub>	–	2400	–	pF	V <sub>DS</sub> = –10V
Output capacitance	C <sub>oss</sub>	–	320	–	pF	V <sub>GS</sub> = 0V
Reverse transfer capacitance	C <sub>rss</sub>	–	200	–	pF	f=1MHz
Turn-on delay time	t <sub>d(on)*</sub>	–	23	–	ns	V <sub>DD</sub> ≐ –25V I <sub>D</sub> = –2.0A
Rise time	t <sub>r</sub> *	–	23	–	ns	V <sub>GS</sub> = –10V
Turn-off delay time	t <sub>d(off)*</sub>	–	90	–	ns	R <sub>L</sub> = 12.5Ω
Fall time	t <sub>f</sub> *	–	22	–	ns	R <sub>G</sub> = 10Ω
Total gate charge	Q <sub>g</sub> *	–	20.0	28.0	nC	V <sub>DD</sub> ≐ –25V, V <sub>GS</sub> = –5V
Gate-source charge	Q <sub>gs</sub> *	–	6.5	–	nC	I <sub>D</sub> = –4.0A
Gate-drain charge	Q <sub>gd</sub> *	–	7.5	–	nC	R <sub>L</sub> = 6Ω, R <sub>G</sub> = 10Ω

\*Pulsed

## ●Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward voltage	V <sub>SD</sub> *	–	–	–1.2	V	I <sub>S</sub> = –4.0A, V <sub>GS</sub> =0V

\*Pulsed

Transistors

N-ch

●Electrical characteristic curves

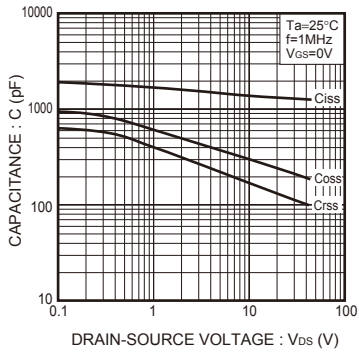


Fig.1 Typical Capacitance vs. Drain-Source Voltage

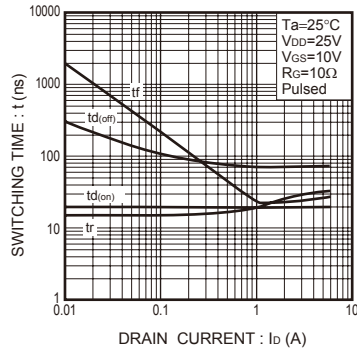


Fig.2 Switching Characteristics

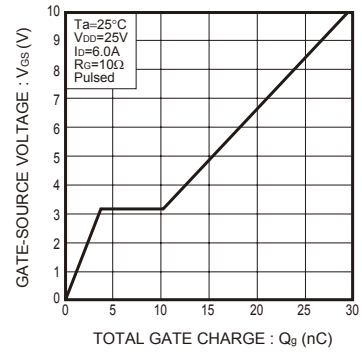


Fig.3 Dynamic Input Characteristics

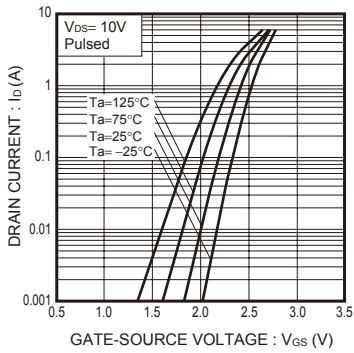


Fig.4 Typical Transfer Characteristics

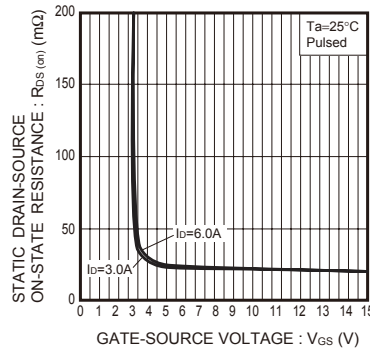


Fig.5 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

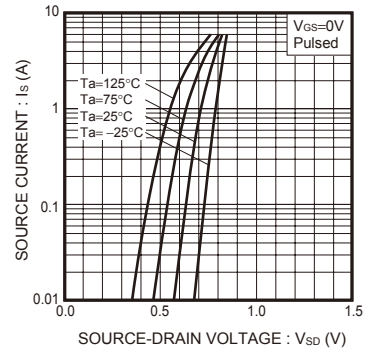


Fig.6 Source Current vs. Source-Drain Voltage

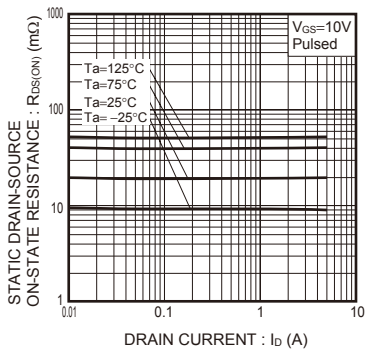


Fig.7 Static Drain-Source On-State Resistance vs. Drain Current ( I )

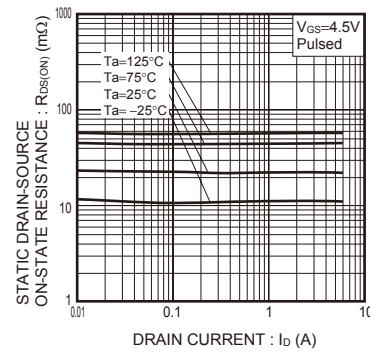


Fig.8 Static Drain-Source On-State Resistance vs. Drain Current ( II )

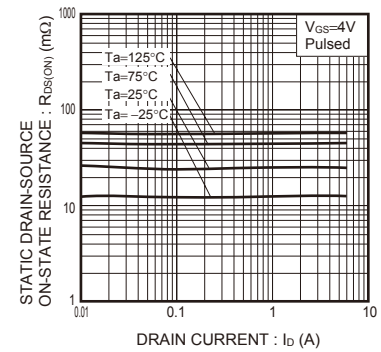


Fig.9 Static Drain-Source On-State Resistance vs. Drain Current ( III )

Transistors

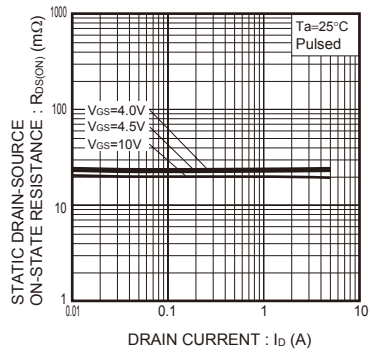


Fig.10 Static Drain-Source On-State Resistance vs. Drain Current (IV)

Transistors

P-ch

●Electrical characteristic curves

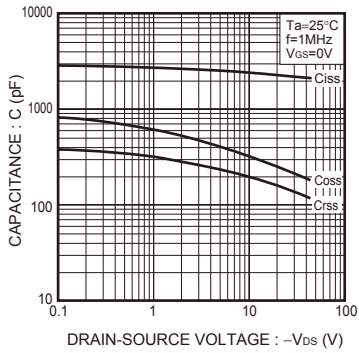


Fig.1 Typical Capacitance vs. Drain-Source Voltage

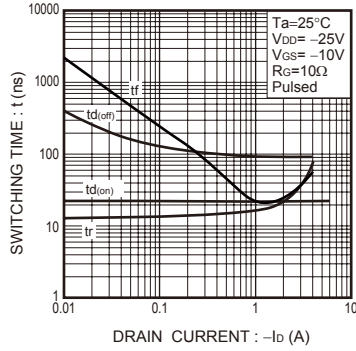


Fig.2 Switching Characteristics

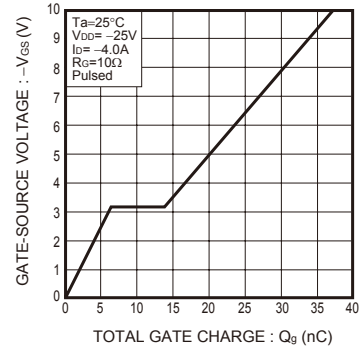


Fig.3 Dynamic Input Characteristics

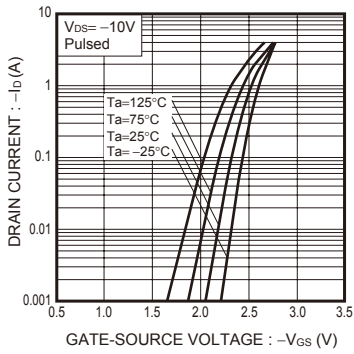


Fig.4 Typical Transfer Characteristics

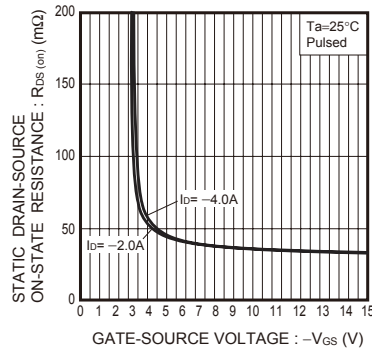


Fig.5 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

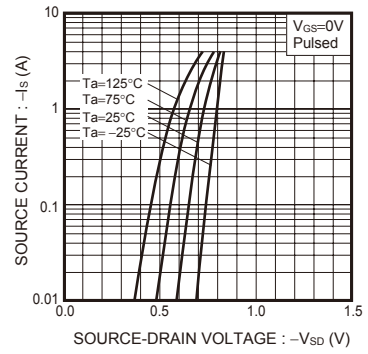


Fig.6 Source Current vs. Source-Drain Voltage

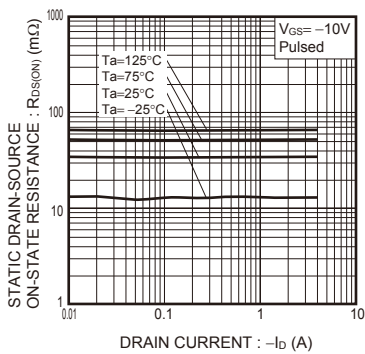


Fig.7 Static Drain-Source On-State Resistance vs. Drain Current ( I )

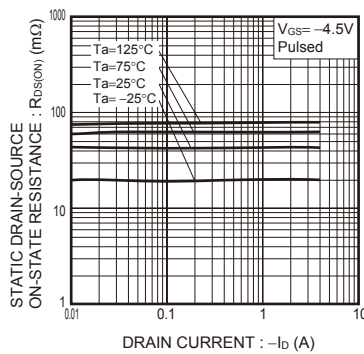


Fig.8 Static Drain-Source On-State Resistance vs. Drain Current ( II )

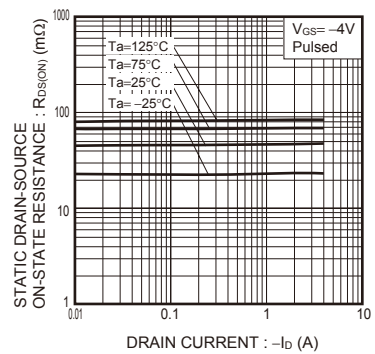


Fig.9 Static Drain-Source On-State Resistance vs. Drain Current ( III )

Transistors

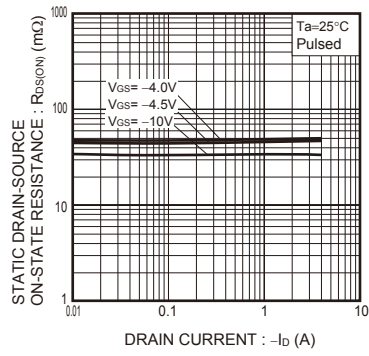


Fig.10 Static Drain-Source On-State Resistance vs. Drain Current (IV)

# Notice

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1. If you intend to use our Products in devices requiring extremely high reliability (such as medical equipment <sup>(Note 1)</sup>, aircraft/spacecraft, nuclear power controllers, etc.) and whose malfunction or failure may cause loss of human life, bodily injury or serious damage to property ("Specific Applications"), please consult with the ROHM sales representative in advance. Unless otherwise agreed in writing by ROHM in advance, ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of any ROHM's Products for Specific Applications.

(Note1) Medical Equipment Classification of the Specific Applications

JAPAN	USA	EU	CHINA
CLASS III	CLASS III	CLASS II b	CLASS III
CLASS IV		CLASS III	

2. ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
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  - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
  - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
4. The Products are not subject to radiation-proof design.
5. Please verify and confirm characteristics of the final or mounted products in using the Products.
6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
8. Confirm that operation temperature is within the specified range described in the product specification.
9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

## Precaution for Mounting / Circuit board design

1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification



### Precautions Regarding Application Examples and External Circuits

1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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### Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of Ionizer, friction prevention and temperature / humidity control).

### Precaution for Storage / Transportation

1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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QR code printed on ROHM Products label is for ROHM's internal use only.

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